

**FORMALISM OF COMMUNICATION BEHAVIOR
ON HANDSHAKE CIRCUIT USING TRACE THEORY**

A THESIS

By

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in obtaining *Sarjana Teknik* Degree in Informatics Engineering Department**

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*To the greatest mom in the whole world
and my beloved one*



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ABSTRACT

A handshake circuit is a network of asynchronous components connected by point-to-point channels along which components interact by means of handshaking signaling. Handshake circuit is formed from some connectable handshake processes. Handshake process itself is a mathematical object that describes a handshake communication behavior. The communication behavior of handshake circuit can be observed by formalize handshake processes. This thesis is about to formalize two handshake processes in order to prove whether they are connectable from each of their communication behavior using trace theory and shows a handshake circuit that are constructed from connectable handshake processes to describe the communication behavior.

Keyword : Handshake circuits, trace theory, parallel process

ABSTRAK

Handshake circuit adalah suatu jaringan yang terdiri dari komponen-komponen asinkron yang terhubung oleh channel point-to-point dimana komponen-komponen tersebut berinteraksi melalui signal handshake. Handshake circuit dibentuk dari beberapa handshake proses. Handshake proses itu sendiri adalah objek matematika yang mendeskripsikan perilaku komunikasi handshake. Perilaku komunikasi dari handshake circuit dapat di observasi dengan cara memformalisasi handshake proses. Dalam tesis ini, penulis akan memformalisasi dua handshake proses untuk membuktikan apakah kedua proses dengan masing-masing perilaku komunikasinya saling berhubungan dengan menggunakan teori trace dan menunjukkan sebuah handshake circuit yang dibentuk dari beberapa handshake proses untuk menjelaskan perilaku komunikasinya..

Kata kunci : Handshake circuits, teori trace, proses parallel

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“I have the strength to face all conditions by the power that Christ gives me”

Philippians 4:13

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LIST OF SYMBOLS

<i>Notations</i>	<i>Meaning</i>	<i>Page</i>
Set Theory		
$\{\dots\}$	set	12
\emptyset	empty set	16
\in	is a member of	16
\notin	is not a member of	16
$A \cap B$	A intersects B	18
$A \cup B$	A union B	19
$A \subseteq B$	A is contained in B	21
$A \setminus B$	set of subtraction from A and B	20
$A \div B$	difference $(A \cup B) \setminus (A \cap B)$	20
Logic		
$=$	equals	12
\neq	is distinct from	13
$\overline{\text{V}}$	end of an example, proof, property, and definition	17
$P \wedge Q$	P and Q (both true)	17
$P \vee Q$	P or Q (one or both true)	25
$P \Rightarrow Q$	if P then Q	12
$\forall x : x \in P(x)$	universal quantification of P(x)	11
$\exists x : x \in P(x)$	existential quantification of P(x)	11
Traces		
Ω	set of universe	16
X^*	set of all finite-length sequences of elements X	16
ε or $\langle \rangle$	empty trace	16
	projection	16

<i>Notations</i>	<i>Meaning</i>	<i>Page</i>
$l(t)$	length of t	17
$s \leq t$	s is prefix of t	17
$pref(X)$	prefix-closure of a trace set X	17
$pref\text{-}\gg$	prefix-closed of a trace set X where $X = pref(X)$	17
\mathbf{aT}	alphabet of T	18
\mathbf{tT}	trace structure of T	18
\mathbf{w}	weaving	19
\mathbf{b}	blending	19
$after(t, T)$	symbols that follow a defined trace $\langle \mathbf{aT}, \{u \mid u \in \mathbf{aT}^* \wedge tu \in \mathbf{tT}\} \rangle$	18
$suc(t, T)$	set of one symbol follow a defined trace $\{a \mid a \in \mathbf{aT} \wedge ta \in \mathbf{tT}\}$	18
$BUF_1(a, b)$	a process of one-place buffer	2

Handshake Processes

$\mathbf{0p}$	request symbol	25
$\mathbf{1p}$	acknowledgement symbol	25
A°	passive port	25
A'	active port	25
\mathbf{iA}	input symbols of port structure A	26
\mathbf{oA}	output symbols of port structure A	26
if .. fi	condition statement	26
[]	else symbol	26
 	then symbol	12
\mathbf{pS}	port structure of S (S is handshake structure)	27
\mathbf{tS}	set of handshake traces	27
A^H	set of handshake traces with port structure A	26
\mathbf{rB}	reordering in B (B is port structure)	28
$(\mathbf{r}) . T$	reorder closed of T	28
\mathbf{x}_B	input extension in B	28

<i>Notations</i>	<i>Meaning</i>	<i>Page</i>
$(\mathbf{x}) . T$	input extension closedness	28
\parallel	parallel composition	36
$\mathcal{D}\ddot{\mathcal{I}}$	connectable	34
$div.\langle A, T \rangle$	divergences of handshake structure $\langle A, T \rangle$	35

Handshake Circuits

Z	activation port of handshake circuit	2
$a?x$	on channel a, input a value of x	3
$b!x$	on channel b, output a value of x	3

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